

# Electron Transport Through Metal–Multiwall Carbon Nanotube Interfaces

Quoc Ngo, Dusan Petranovic, Shoba Krishnan, Alan M. Cassell, Qi Ye, Jun Li, M. Meyyappan, *Fellow, IEEE*, and Cary Y. Yang, *Fellow, IEEE*

**Abstract**—In this paper, we examine mechanisms of electron transport across the metal–carbon nanotube (CNT) interface for two different types of multiwall carbon nanotube (MWNT) architectures, horizontal or side-contacted MWNTs and vertical or end-contacted MWNTs. Horizontally aligned nanotube growth and electrical characteristics are examined with respect to their potential applications in silicon-based technologies. Recent advances in the synthesis techniques of vertical MWNTs have also enhanced the possibility for a manufacturable solution incorporating this novel material as on-chip interconnects or vias as copper interconnect feature sizes are scaled into the sub-100-nm regime. A vertical MWNT architecture is presented that may be suitable for integration into silicon-based technologies. The growth method for this architecture and its effect on electrical characteristics are examined. Through simulations, dc measurements, and comparison of our results with previous studies, we explain why high contact resistance is observed in metal–CNT–metal systems.

**Index Terms**—Carbon nanotube (CNT), contact resistance, on-chip interconnect.

## I. INTRODUCTION

CARBON NANOTUBES (CNTs) exhibit unique electronic properties and extraordinary mechanical properties and have received much attention in the nanoelectronics community. Fabrication of field-effect transistors using single-walled carbon nanotubes (SWNTs) as conducting channels has been demonstrated [1], [2]. The very high current-carrying capacity of multiwall carbon nanotubes (MWNTs) has prompted investigations of their use as interconnects in silicon technology [3]. In all of these applications, the nanotube–metal contact resistance is an important parameter that needs to be minimized. Unlike the near-ohmic behavior of metal contacts and interconnects, contact resistance at the metal–CNT junction dominates the electrical performance of CNT wires. This interaction is largely dependent on physical design and process parameters such as electrical contact area [4], different electrode materials used [4]–[7], and the structure of the concentric shells within the MWNT. The ability to consistently fabricate high aspect-ratio structures such as vertical MWNTs makes them an excellent

candidate to complement and improve upon copper-based interconnect technology. Successful integration of such architectures into current fabrication processes, however, requires that, among others, contact resistance be minimized so that the full benefit of ballistic transport in MWNTs can be exploited.

Growth processes can define both electrical and mechanical characteristics of a nanotube structure. The methods in which CNTs are fabricated have a direct impact on the contact characteristics between a metal electrode and multiwall nanotubes. The primary growth methods for MWNTs include arc discharge and chemical vapor deposition (CVD). In Section II, we explain the different types of MWNT growth methods and explore the structure–property relationship between the method of growth and electron transport across metal–CNT junctions. The various growth techniques and their effects on electrical characteristics of the MWNT are explored.

It is important to note that, for both side-contacted and end-contacted nanotubes (Figs. 1 and 2), we expect distinctly different mechanisms of electron transport across the metal–CNT interface. Side-contacted CNTs depend upon tunneling of electrons across a finite physical barrier created by van der Waals interaction at the metal–CNT interface [8]. The contact quality for end contacts depends on chemical (chemisorption) and/or physical (physisorption) processes at the nanotube tip. The implication of the two MWNT architectures on contact quality is discussed in Section III.

Measured resistance in metal–CNT–metal systems varies widely among experiments and is often much larger than its theoretical value [9]. Since high metal–CNT contact resistance limits the practical application of CNTs, it is important to be able to make reliable low-resistance contacts. Some novel ideas for optimizing measurement techniques and minimizing contact resistance have been presented in previous studies, such as electron bombardment used to enhance transmission of electrons between the metal and CNT [11], and using ropes of SWNTs to conduct four-point probe measurements in order to deembed probe resistance, subsequently reducing total resistance [12]. For the type of geometry presented in [11] and [12], tight-binding calculations [4], [5] have shown that contact area affects the transmission of electrons across the side-contacted metal–CNT junction, hence, directly impacting contact resistance. The configurations presented in [11] and [12] can be characterized as being side contacted, similar to state-of-the-art on-chip copper interconnects. Simulation results obtained by generalizing metal–insulator–metal (MIM) theory [13] for side-contacted metal–CNT systems are shown in Section IV.

Manuscript received August 18, 2003; revised January 18, 2004. An earlier version of this paper was presented at the 2003 IEEE Conference on Nanotechnology, August 12–14, 2003.

Q. Ngo, D. Petranovic, S. Krishnan, and C. Y. Yang are with the Center for Nanostructures, Santa Clara University, Santa Clara, CA 95050 USA (e-mail: qngo1@scu.edu).

A. M. Cassell, Q. Ye, J. Li, and M. Meyyappan are with the National Aeronautics and Space Administration, Ames Research Center, Center for Nanotechnology, Moffett Field, CA 94035 USA.

Digital Object Identifier 10.1109/TNANO.2004.828553

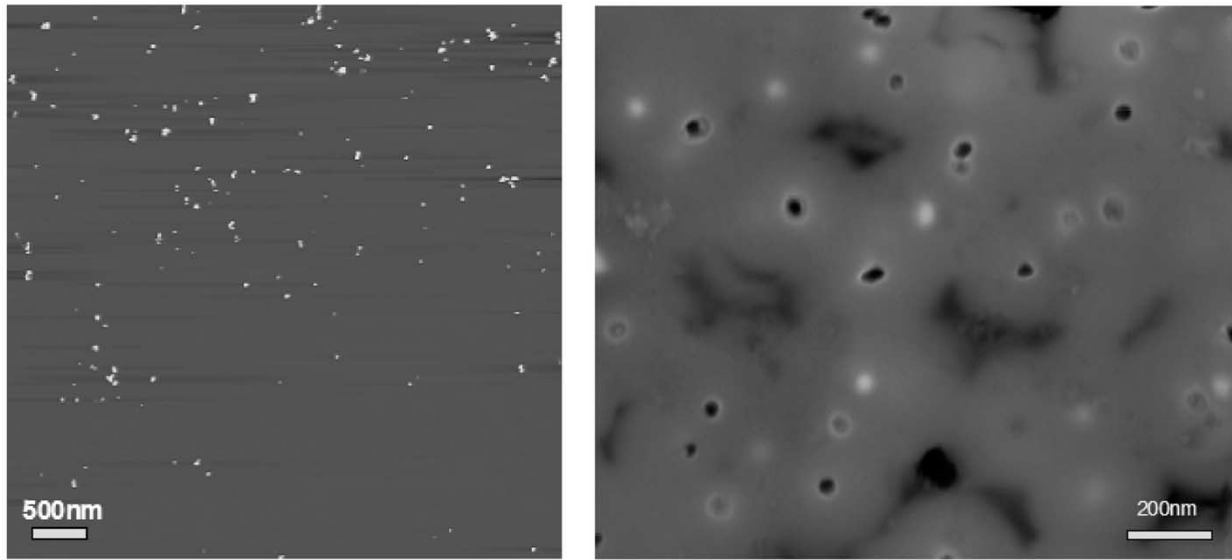


Fig. 1. (a) Current-sensing AFM image of end-contacted MWNT configuration. (b) Scanning electron microscope (SEM) top view. In (a), the lighter areas represent MWNTs embedded in an  $\text{SiO}_2$  matrix (represented by the dark background). In (b), the circular structures represent top views of vertical MWNTs.

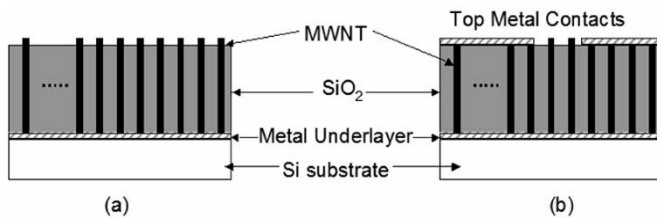


Fig. 2. Schematic cross section of: (a) pre-metal deposition and (b) post-metal deposition vertical MWNT.

Analyzing metal–CNT contact characteristics of vertical nanotube structures requires a different fundamental approach to understand electron transport at the interface. Vertically aligned nanotubes [14], shown in Fig. 2(a), can be characterized as having end-contacted geometry since electrically connecting these structures requires a conductive material contacting the end of the MWNT. Following the deposition of metal electrodes over a finite area of vertical MWNTs, as shown in Fig. 2(b), we demonstrate through resistance characterization the possibility of using vertically aligned CNTs for on-chip interconnect applications. The details and results of the experiment to obtain dc measurements are discussed in Section V. In Section VI, we compile some previous studies of resistance in MWNT systems and analyze these results with respect to the theory and experiment presented in Sections II–V.

## II. MWNT GROWTH

Creating multiwall nanotubes using the arc-discharge method [16] produces high-quality samples of 2–200 nm in diameter and 1–100  $\mu\text{m}$  in length. The parallel nature of the nanotube walls demonstrated by Iijima [16] is ideal for facilitating ballistic conduction within the CNT. MWNTs grown using the arc-discharge method suffer from two drawbacks when applied to interconnect applications. Arc-discharge-grown nanotubes are produced in soot form, mixed with various other forms of carbon materials, making it necessary for additional processing

steps such as purification, sonication, and dispersion onto a substrate to be performed. In addition, nanotubes grown using this method are primarily laid horizontally on a flat substrate in a slow and random process, restricting the nanotubes to side-contacted electron transport across a Schottky barrier formed at the metal–CNT junction. To circumvent such issues in ultra-large scale integration (ULSI), a more robust and consistent architecture for nanotube growth must be used.

CVD growth of nanotubes [3], [14] has been shown to yield promising electrical characteristics for integration into on-chip interconnect systems. The ability to grow well-aligned nanotube arrays using standard microelectronic fabrication techniques [17]–[19], [21] opens up many possibilities for applications to silicon technology. Within the broad category of CVD growth of MWNTs, there are two variations that have been discussed in the literature: plasma-enhanced chemical vapor deposition (PECVD), and thermal CVD. The growth of CNTs using thermal CVD normally results in “spaghetti-like” CNTs, as shown in [3]. In the PECVD process, an additional measure of nanotube orientation control is introduced by inducing the electric field normal to the substrate surface such that the direction of the nanotube can be uniformly aligned along the direction of the electric field [19]. We must note, however, there are some properties of CNTs grown by the CVD process that are not desirable for electronic applications. Undesired structural properties resulting from the PECVD process can be seen at the nanotube tip using transmission electron microscope (TEM) analysis of the nanotube. The pear-shaped catalyst particle reveals that the concentric shells of a CNT grown using the PECVD process form a bamboo-like structure, which is observed throughout the entire length of the nanotube [20], [21]. This type of structure obtained from plasma-enhanced growth was examined in [21] by increasing capacitive bias on the substrate, which resulted in an increase in atomic hydrogen intensity. The unique physical property of the PECVD-grown nanotube inhibits ballistic conduction through the MWNT by forcing electrons to “hop” across multiple

graphitic layers to facilitate electrical conduction along the nanotube axis.

### III. IMPLICATIONS OF CONTACT GEOMETRIES ON THE RESISTANCE OF MULTIWALL NANOTUBES

#### A. Side-Contacted Metal–CNT Junction

Contact resistance in traditional CMOS on-chip interconnect technology has long been modeled by studying well-known interfaces between different types of bulk materials. The merits of such models can be validated through experimental techniques such as kelvin structures for resistivity measurements [22]. In deep submicrometer circuits, current crowding is a phenomenon seen at the narrowing or bend of a copper interconnect line or via, causing carriers to flow predominantly near the “corners” of such a structure. Since CNTs are treated as one-dimensional systems, the current-crowding mechanism is less likely to be observed in metal–CNT systems.

The transport mechanism across a side-contacted metal–CNT junction is explained in [6] as tunneling across an energy barrier created by the finite separation between the gold electrode and MWNT. Combining this theory with observations that the wave function orthogonal to the nanotube axis decays rapidly away from the CNT surface [5], we conclude that electron injection into the nanotube is accomplished by quantum-mechanical tunneling through this physically induced energy barrier at the metal–CNT junction. Since the physical separation between metal and CNT is almost the same as the bond length between carbon and the metal atom, tunneling depends on the chemical composition and configuration of electronic states at the surface. The wave-function overlap is reduced by this gap-induced energy barrier, and transmission is decreased for side-contacted CNTs with small contact area. For large contact area, electronic structure calculations suggest better electronic coupling between the electrode material and MWNT [4], [5].

#### B. End-Contacted Metal–CNT Junction

Similar to the side-contacted geometry, the chemistry at the end-contacted metal–CNT junction must be well understood. The TEM analysis of the MWNT tip structure allows us to understand how the physical properties of the nanotube affect electron movement through metal–CNT–metal systems. The presence of dangling bonds at the end of a multiwall nanotube after oxide planarization provides an opportunity for chemisorption of the CNT by the electrode material to occur. The end-contacted structure shown in Fig. 2 represents a possible architecture for using MWNTs as on-chip interconnects. The size and vertical nature of the aligned array make them good candidates for use as vias, bypassing processing problems for high aspect-ratio structures with current silicon-based technology, while retaining desirable electronic properties. In addition, this bottom-up approach for synthesizing CNTs allows for silicon-compatible processing, ideal for integration into current wafer manufacturing processes. As MWNT growth processes mature, the initial step for implementing them into silicon-based systems would be to replace copper via technology with a similar CNT-based via structure [3].

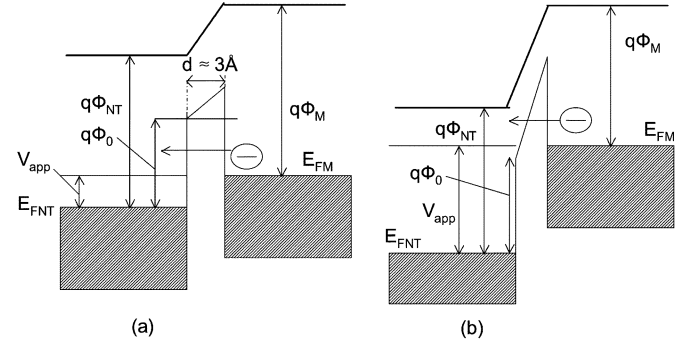


Fig. 3. Energy-band diagram for metal–CNT interface under different bias conditions. The main parameters for modeling this junction as a MIM junction are: finite separation distance ( $d$ ), the energy barrier height ( $\Phi_0$ ), and the electrode work function ( $E_{FM}$ ). (a)  $0 < V_{app} < \Phi_0$ . (b)  $V_{app} > \Phi_0$ .

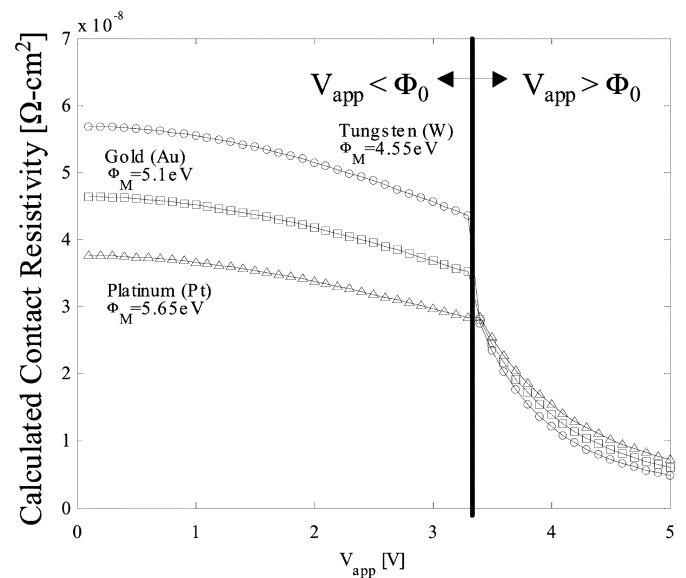


Fig. 4. Calculated contact resistivity across the metal–CNT interface using MIM tunneling formulation. When higher voltage is applied ( $V_{app} > \Phi_0$ ), the effects of FN tunneling will be observed.

### IV. SIMULATION OF CONTACT RESISTIVITY VERSUS APPLIED VOLTAGE FOR SIDE-CONTACTED GEOMETRIES

Fig. 3 shows an energy-band diagram illustrating how electron transport across the metal–CNT junction is achieved. To predict the behavior of this effect on contact resistance, we modify the approach presented in [13] for a MIM system. We know that bulk properties will not always be observed in thin-film systems such as the metal–CNT interface under consideration. We can, however, gain some insight by finding a correlation between different tunneling mechanisms at the interface and how they affect contact resistivity. Contact resistivity is presented in the context of applied voltage across the nanotube. The band diagram for the contact–CNT interface in Fig. 3 is for two different applied bias conditions. The finite separation was approximated [8] by a van der Waals interaction at the metal–CNT interface. For our analysis, we neglect hot-electron effects such as thermionic emission that is more prominent in transistors. The following closed-form expression used to describe this junction was derived from the tunneling

TABLE I  
SINGLE MWNT RESISTANCE IN RECENT STUDIES

Electrode Configuration (metal-CNT configuration)	Electrode Material	Overall Resistance	Reference
Metal-on-tube (side-contact)	W	1.7kΩ <sup>‡</sup>	[10]
Metal-on-tube (side-contact)	Ti	28kΩ	[29]
Metal-on-tube (side-contact)	Pt/Au	13kΩ	[29]
Metal-on-tube (side-contact)	Pt*	50kΩ	[26]
Point contact (end contact)	Pt <sup>†</sup>	50kΩ-300kΩ	[14,15]
Buried nanotube (side + end contact)	Ti/Au	~500Ω	[27]
Deposited metal contact (end-contact)	Cr	11kΩ	This work

\*Actual contact is made to platinum via pre-deposited aluminum pads

<sup>†</sup>Electrode is a platinum covered, current-sensing atomic force microscope (CSAFM) tip, second contact is chromium

<sup>‡</sup> Four-terminal measurement

current density equation presented in [24], originally used to analyze metal–semiconductor heterojunctions:

$$J = \frac{q}{4\pi^2\hbar} \iint T_t [F_1(E) - F_2(E)] dk^2 dE. \quad (1)$$

Here,  $T_t$  is the tunneling probability and  $F_1$  and  $F_2$  are the Fermi distribution functions in the conducting regions (MWNT and metal contact). We integrate over the wave vector and energy domains where tunneling occurs.

Three electrode materials, gold, platinum, and tungsten are studied using this method. The work-function differences in these materials show a distinct effect on contact resistance. Fig. 4 presents results obtained based on (1). Most dc current–voltage ( $I$ – $V$ ) measurements in the low-bias range ( $<1$  V) have shown that contact resistance values remain relatively constant with respect to applied voltage. According to our bias-dependent contact resistivity predictions, this is expected behavior at low bias. At high bias, electron transport resembles Fowler–Nordheim (FN) [25] tunneling, as depicted in Fig. 3(b). Reference [10] reports data for resistance in the high-bias range (25 V). At this applied voltage, our tunneling model would predict a marked decrease in contact resistivity, consistent with the high-bias result observed [10]. Whether CNT wires will be biased at such high voltages for interconnect applications is yet unknown. However, to realize the full potential range of applications for MWNTs, it is imperative to study the dependence of  $R_C$  over a wide range of applied voltages.

## V. COMPARISON AND ANALYSIS OF RESISTANCE IN MWNT SYSTEMS

The work presented in [7] reflects a systematic approach for analyzing the dependence of CNT resistance on a variety of different factors including electrode metal, bonding configuration, and the type of nanotube (SWNT or MWNT). Our research summarizes more recent studies, as well as the work presented here, that focus on MWNT systems and discusses

the manner in which the electrode was configured with respect to the CNT. Recognizing that some of the experiments summarized in Table I were not specifically targeted for contact resistance measurement, we will qualify our discussion accordingly.

Table I reveals the many variations seen for the metal-on-tube (MOT) configuration. In all of the MOT studies, the nanotube was placed on a silicon substrate, and metallic contacts were deposited using either focused ion-beam lithography [10], [26], or e-beam lithography [27]. The high resistance observed for titanium contact [29] is a result of the coulomb blockade (CB) effect and the material properties of titanium, a metal susceptible to oxidation when exposed to air. For realistic integrated circuit (IC) applications, however, CNTs should be studied under ambient conditions. Considering only direct and FN tunneling, materials with higher work functions contributes to more efficient electron injection, thereby exhibiting more desirable resistance properties for side-contacted geometry. Fig. 5 shows an energy-band diagram depicting the work-function dependence of electron injection into the CNT. Low work-function materials, such as tungsten, suffer from higher resistance across the interface as compared to high work-function materials because higher voltages need to be applied to inject electrons into the nanotube. To induce an electric field that facilitates tunneling into the nanotube, as in Fig. 5(b), a sufficient voltage ( $V_{app} > \Phi_{NT} - \Phi_W$ ) needs to be applied to the tungsten electrode.

Work function is by no means the exclusive factor in determining differences in contact resistance. The quality and method in which the contacts were fabricated must also be analyzed to interpret the results of these experiments. Perhaps the most striking study was [27] due to the small value obtained for CNT resistance. The authors introduced a method for “burying” the MWNT under a layered contact constructed using titanium and gold. The resulting low resistance indicates that multiple walls, i.e., concentric nanotubes, are being contacted in parallel. To enable us to bring the resistance down to a manageable level for practical use in on-chip interconnect

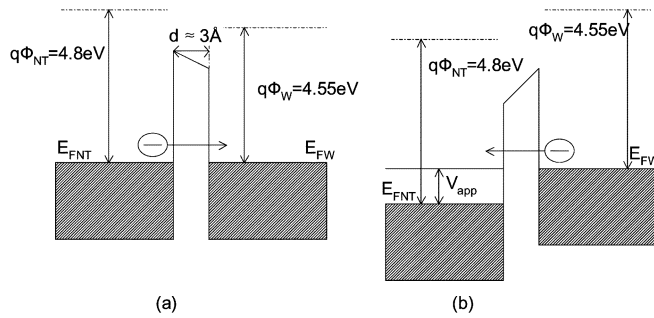


Fig. 5. Energy-band diagram for CNT–tungsten interface under different bias conditions. (a) Effect of the work-function difference between tungsten ( $\Phi_W = 4.55 \text{ eV}$ ) and CNT ( $\Phi_{CNT} = 4.8 \text{ eV}$  [28]). (b) Bias condition sufficient for facilitating tunneling into the CNT. (a)  $V_{app} = 0V$ . (b)  $V_{app} > (\Phi_{NT} - \Phi_W)$ .

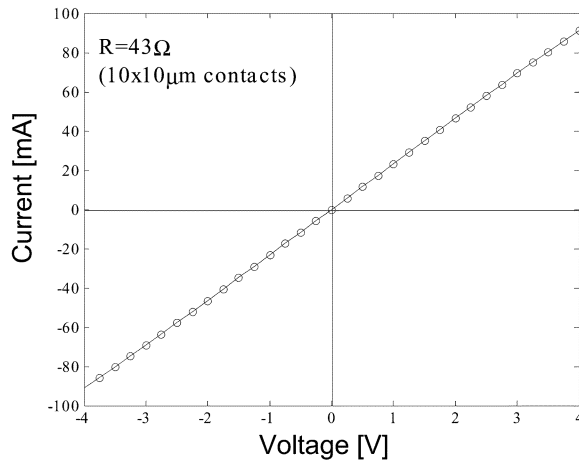


Fig. 6. Typical  $I$ – $V$  curve for parallel MWNTs connected in series through two  $10 \times 10 \mu\text{m}$  chromium contact pads.

systems, methods need to be developed to optimize the number of walls that couple to the metal contact.

## VI. CHARACTERIZATION OF PECVD-GROWN VERTICAL MWNTs FOR INTERCONNECT APPLICATIONS

To demonstrate the viability of implementing CNTs grown using the PECVD process in interconnect systems, we present results of electrical characterization of multiple MWNTs connected in parallel. For our study, we use  $2000 \text{ \AA}$  of chromium as the metal on which the catalyst film is patterned. Nanotubes are then grown using the PECVD process described in Section II. For consistency, we also use chromium as the top metal layer. The completed structure looks similar to the schematic representation shown in Fig. 2. The top deposited metal layer was patterned using e-beam lithography to obtain  $10 \times 10 \mu\text{m}^2$  chromium squares with a thickness of  $2000 \text{ \AA}$  that make contact to the tips of the MWNTs. Fig. 6 shows a typical  $I$ – $V$  curve of nanotubes contacted through two contact pads. The near-ohmic behavior of the curve suggests that parallel MWNTs may provide performance comparable to copper vias for on-chip interconnect architectures without the processing difficulties presented by current state-of-the-art copper deposition technology. The sheet resistance of the chromium underlayer was also considered in our study. An area of bare chromium, subjected to the same growth conditions as the CNTs, was characterized using two-

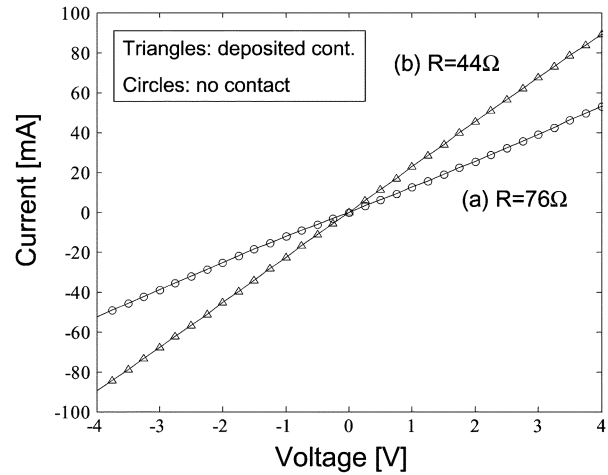


Fig. 7.  $I$ – $V$  curve comparing parallel MWNTs: (a) contacted directly with a  $25\text{-}\mu\text{m}$ -diameter probe and (b) with deposited  $10 \times 10 \mu\text{m}$  contact pads.

and four-terminal measurements. The resulting chromium resistance for the two-terminal measurement was  $10\text{--}15 \Omega$ , while the four-terminal measurement yielded  $4\text{--}6 \Omega$ , most of which can be attributed to the contact–Cr interface. While these resistance values seem small, if we compare the two-terminal  $I$ – $V$  measurement of the nanotubes to the contact–Cr resistance, we can see that the proportion of contact resistance to overall resistance is quite high.

The importance of depositing a quality contact becomes apparent when we compare directly contacting parallel MWNTs with a  $25\text{-}\mu\text{m}$ -diameter tungsten probe tip to contacting nanotubes through the patterned  $10 \times 10 \mu\text{m}^2$  contacts. Fig. 7 shows the stark contrast in resistance values when considering the deposition of metal contacts over multiple nanotubes. This result suggests that there is a distinct chemical and physical interaction between the deposited top metal layer and the end of the MWNT that creates a less resistive metal–CNT junction. Dangling bonds at the nanotube tip after chemical mechanical polishing (CMP) enhance the possibility of creating a quality metal–CNT contact using metal deposition. A similar study using thermal CVD to grow nanotubes has been conducted in [3], however, the structural [23] and electrical integrity [14] of such devices under standard microelectronic fabrication techniques such as CMP must also be considered.

An inherent obstacle for characterizing single nanotubes is the limitation of currently available instruments, specifically the atomic force microscope (AFM). Typical AFM probe tips to perform  $I$ – $V$  measurements are  $\sim 10 \text{ nm}$  in diameter. The MWNTs under study here are  $50\text{--}100 \text{ nm}$  in diameter, as can be seen in Fig. 1(b). The mismatch of the probe tip and MWNT diameter alters the direct measurement of electrical characteristics (i.e., AFM probe to the end of a MWNT). Resistances of  $50\text{--}300 \text{ k}\Omega$  [14] have been reported using this methodology. In this study, we use a statistical approach by measuring the  $I$ – $V$  curve of many parallel nanotubes. Using current-sensing atomic force microscope (CSAFM) imaging, we can approximate the number of nanotubes per unit area. From our analysis, there are  $5\text{--}6$  MWNTs per  $1 \mu\text{m}^2$ , corresponding to a resistance of  $11 \text{ k}\Omega\text{--}13.2 \text{ k}\Omega$  per single nanotube since we are measuring two sets of MWNTs in series. This type of analysis for single

and multiple MWNTs has direct relevance to application of nanotubes to interconnect systems since the structure used in this study is similar to copper via technology.

## VII. CONCLUSION

We have presented an analysis of transport mechanisms related to contact resistance in metal-CNT systems. Our study has compared and contrasted the different mechanisms of electron transport across the contact interface. Based on our analysis, a MIM model traditionally used for two conductive materials separated by an ultra-thin barrier layer has been proposed. Simulation and experimental data have been presented to demonstrate the utility of MWNTs for on-chip interconnect systems.

## REFERENCES

- [1] R. Martel, T. Schmidt, H. R. Shea, T. Hertel, and P. Avouris, "Single- and multi-wall carbon nanotube field-effect transistors," *Appl. Phys. Lett.*, vol. 73, pp. 2447–2449, Oct. 1998.
- [2] C. Zhou, J. Kong, and H. Dai, "Electrical measurements of individual semiconducting single-walled carbon nanotubes of various diameters," *Appl. Phys. Lett.*, vol. 76, pp. 1597–1599, Mar. 2000.
- [3] F. Kreupl, A. P. Graham, G. S. Duesberg, W. Steinhogel, M. Liebau, E. Unger, and W. Honlein, "Carbon nanotubes in interconnect applications," *Microelectron. Eng.*, vol. 64, pp. 399–408, 2002.
- [4] M. P. Anantram, "Which nanowire couples better electrically to a metal contact: Armchair or zigzag nanotube?," *Appl. Phys. Lett.*, vol. 78, pp. 2055–2057, Apr. 2001.
- [5] M. P. Anantram, S. Datta, and Y. Xue, "Coupling of carbon nanotubes to metallic contacts," *Phys. Rev. B, Condens. Matter*, vol. 61, pp. 14219–14224, May 2000.
- [6] J. Tersoff, "Contact resistance of carbon nanotubes," *Appl. Phys. Lett.*, vol. 74, pp. 2122–2124, Apr. 1999.
- [7] A. N. Andriotis, M. Menon, and G. E. Froudakis, "Various bonding configurations of transition-metal atoms on carbon nanotubes: Their effect on contact resistance," *Appl. Phys. Lett.*, vol. 76, pp. 3890–3892, June 2000.
- [8] A. Rochefort, P. Avouris, F. Lesage, and D. R. Salahub, "Electrical and mechanical properties of distorted carbon nanotubes," *Phys. Rev. B, Condens. Matter*, vol. 60, pp. 13824–13830, Nov. 1999.
- [9] M. Buttiker, Y. Imry, R. Landauer, and S. Pinhas, "Generalized many-channel conductance formula with application to small rings," *Phys. Rev. B, Condens. Matter*, vol. 31, pp. 6207–6215, May 1985.
- [10] B. Q. Wei, R. Vajtai, and P. M. Ajayan, "Reliability and current carrying capacity of carbon nanotubes," *Appl. Phys. Lett.*, vol. 79, pp. 1172–1174, Aug. 2001.
- [11] A. Bachtold, M. Henny, C. Terrier, C. Strunk, C. Schonenberger, J.-P. Salvetat, J.-M. Bonard, and L. Forro, "Contacting carbon nanotubes selectively with low-ohmic contacts for four-probe electric measurements," *Appl. Phys. Lett.*, vol. 73, pp. 274–276, July 1998.
- [12] J. Appenzeller, R. Martel, P. Avouris, H. Stahl, and B. Lengeler, "Optimized contact configuration for the study of transport phenomena in ropes of single-wall carbon nanotubes," *Appl. Phys. Lett.*, vol. 78, pp. 3313–3315, May 2001.
- [13] J. G. Simmons, "Generalized formula for the electric tunnel effect between similar electrodes separated by a thin insulating film," *J. Appl. Phys.*, vol. 34, pp. 1793–1803, June 1963.
- [14] J. Li, Q. Ye, A. Cassell, H. T. Ng, R. Stevens, J. Han, and M. Meyyappan, "Bottom-up approach for carbon nanotube interconnects," *Appl. Phys. Lett.*, vol. 82, pp. 2491–2493, Apr. 2003.
- [15] J. Li, R. Stevens, L. Delzeit, H. T. Ng, A. Cassell, J. Han, and M. Meyyappan, "Electronic properties of multiwalled carbon nanotubes in an embedded vertical array," *Appl. Phys. Lett.*, vol. 81, pp. 910–912, July 2002.
- [16] S. Iijima, "Helical microtubules of graphitic carbon," *Nature*, vol. 354, pp. 56–58, 1991.
- [17] L. Delzeit, C. V. Nguyen, B. Chen, R. Stevens, A. Cassell, J. Han, and M. Meyyappan, "Multiwalled carbon nanotubes by chemical vapor deposition using multilayered metal catalysts," *J. Phys. Chem. B*, vol. 106, pp. 5629–5635, 2002.
- [18] Z. P. Huang, J. W. Xu, Z. F. Ren, J. H. Wang, M. P. Siegal, and P. N. Provencio, "Growth of highly oriented carbon nanotubes by plasma-enhanced hot filament chemical vapor deposition," *Appl. Phys. Lett.*, vol. 73, pp. 3845–3847, Dec. 1998.
- [19] C. Bower, W. Zhu, S. Jin, and O. Zhou, "Plasma-induced alignment of carbon nanotubes," *Appl. Phys. Lett.*, vol. 77, pp. 830–832, Aug. 2000.
- [20] W. D. Zhang, Y. Wen, J. Li, G. Q. Xu, and L. M. Gan, "Synthesis of vertically aligned carbon nanotubes films on silicon wafers by pyrolysis of ethylenediamine," *Thin Solid Films*, vol. 422, pp. 120–125, 2002.
- [21] L. Delzeit, I. McAninch, B. A. Cruden, D. Hash, B. Chen, J. Han, and M. Meyyappan, "Growth of multiwall carbon nanotubes in an inductively coupled plasma reactor," *J. Appl. Phys.*, vol. 91, pp. 6027–6033, May 2002.
- [22] W. M. Loh, S. E. Swirhun, T. A. Schreyer, R. M. Swanson, and K. C. Saraswat, "Modeling and measurement of contact resistances," *IEEE Trans. Electron Devices*, vol. ED-34, pp. 512–524, Mar. 1987.
- [23] J. Li, Q. Ye, A. Cassell, J. Koehne, H. T. Ng, J. Han, and M. Meyyappan, "Carbon nanotube interconnects: A process solution," in *Proc. Int. Interconnect Technology Conf.*, San Francisco, CA, 2003, pp. 271–272.
- [24] L. L. Chang, P. J. Stiles, and L. Esaki, "Electron tunneling between a metal and semiconductor: Characteristics of Al–Al<sub>2</sub>O<sub>3</sub>–SnTe and –GeTe junctions," *J. Appl. Phys.*, vol. 38, p. 4440, Oct. 1967.
- [25] R. H. Fowler and L. W. Nordheim, "Electron emission in intense electric fields," *Proc. R. Soc. Lond. A, Math. Phys. Sci.*, vol. 119, pp. 173–181, 1928.
- [26] S. B. Schujman, R. Vaitaj, S. Biswas, B. Dewhirst, and L. J. Schowalter, "Electrical behavior of isolated multiwall carbon nanotubes characterized by scanning surface potential microscopy," *Appl. Phys. Lett.*, vol. 81, pp. 541–543, July 2002.
- [27] P. J. de Pablo, E. Graugnard, B. Walsh, R. P. Andres, S. Datta, and R. Reifengerger, "A simple, reliable technique for making electrical contact to multiwalled carbon nanotubes," *Appl. Phys. Lett.*, vol. 74, pp. 323–325, Jan. 1999.
- [28] J. Zhao, J. Han, and J. P. Lu, "Work functions of pristine and alkali-metal intercalated carbon nanotubes and bundles," Univ. North Carolina at Chapel Hill, Chapel Hill, NC, Cond-Mat/0 111 103, 2001.
- [29] A. Kanda, Y. Ootuka, K. Tsukagoshi, and Y. Aoyagi, "Electron transport in metal/multiwall carbon nanotube/metal structures (metal = Ti or Pt/Au)," *Appl. Phys. Lett.*, vol. 79, pp. 1394–1396, Aug. 2001.



**Quoc Ngo** received the B.S. degree in electrical engineering from Oregon State University, Corvallis, in 2001, the M.S. degree in electrical engineering from Santa Clara University, Santa Clara, CA, in 2003, and is currently working toward the Ph.D. degree in electrical engineering at Santa Clara University.

From 1997 to 2002, he held summer internships with the Intel Corporation during which time he was involved with yield analysis, defect metrology, back-end integration, and interconnect research and development for developmental 300-mm processes.

He is currently collaborating with the Center for Nanotechnology, National Aeronautics and Space Administration (NASA) Ames Research Center, Moffett Field, CA, to participate in an initiative for implementation of CNTs into silicon-based technology. His primary research interest includes synthesis and modeling of CNT on-chip interconnects. He is also involved in developing a compact model for MOSFET gate currents.



**Dusan Petranovic** received the B.S. degree in electrical engineering from the University of Belgrade, Belgrade, Yugoslavia, in 1976, the M.S. degree in computer engineering from the Worcester Polytechnic Institute, Worcester, MA, in 1979, and the Ph.D. degree from the University of Montenegro, Montenegro, Yugoslavia, in 1986.

He was an Assistant and Associate Professor with the University of Montenegro. He was also an Adjunct Professor with the University of Belgrade. He was an Electrical Engineering Department Dean and Chair with the University of Montenegro. He spent six years teaching at Harvey Mudd College, Claremont, CA. In 1997, he joined the LSI Logic Advanced Development Laboratory, as a Member of Technical Staff, where he was involved with process research and development, and worked on interconnect modeling for high-speed digital circuit design. He is currently with the Center for Nanostructures, Santa Clara University, Santa Clara, CA. He was a consultant for National Aeronautics and Space Administration (NASA) on aircraft control law design, and for NOVA Management Inc. on a design of a Tera FLOPS digital signal processor. He has authored or coauthored over 30 international journal and conference papers. He holds eight U.S. patents.

Dr. Petranovic was the recipient of a one-year Fulbright Grant.



**Shoba Krishnan** received the B.Tech. degree from the Jawaharlal Nehru Technological University, New Dehli, India, in 1987, and the M.S. and Ph.D. degrees from Michigan State University, East Lansing, in 1990 and 1993 respectively.

From 1995 to 1999, she was with the Mixed-Signal Design Group, LSI Logic Corporation, Milpitas, CA, where she was involved with high-speed data communication integrated-circuit (IC) design and testing. She is currently an Assistant Professor with the Department of Electrical Engineering, Santa Clara University, Santa Clara, CA. Her current research interests include analog and mixed-signal IC design and testing and the study of signal integrity and modeling issues in mixed-mode ICs. Her research currently extends to emerging technologies such as CNTs for application toward future use in ICs.



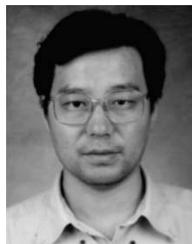
**Alan M. Cassell** received the B.S. degree in chemistry from the University of South Carolina, Spartanburg, in 1993, and the Ph.D. degree in organic chemistry from the University of South Carolina, Columbia, in 1997, where he studied the synthesis and applications development of carbon nanomaterials.

In 1998, he joined the National Aeronautics and Space Administration (NASA) Ames Center for Nanotechnology, Moffett Field, CA, where he jointly performed post-doctoral research on CVD approaches for the synthesis of CNTs until 1999. Since then, he has been a Senior Research Scientist with the Center for Nanotechnology, NASA Ames Research Center, where he develops PECVD approaches for carbon nanomaterials, along with high throughput methodology to rapidly accelerate applications development.



**Qi Ye** received the Ph.D. degree in materials science and engineering from the University of Minnesota at Minneapolis–St. Paul, in 1997.

From 2002 to 2004, she was with the Eloret Corporation. From 1997 to 1999, she was a Post-Doctoral Research Scientist with the Lawrence Berkeley National Laboratory, where she was involved with MOCVD growth and characterization of GaN-based blue LED laser devices. While working on microelectromechanical system (MEMS) research and development with General Nanotechnology, she designed and fabricated silicon, silicon–nitride, and diamond-coated silicon cantilever tips for AFM applications. While with Onix Microsystems, she was a Research and Development Engineer involved with the design and fabrication of three-dimensional (3-D) micromirror MEMS-based switch devices for fiber-optical network applications. In April 2002, she joined the Center for Nanotechnology, NASA Ames Research Center, Moffett Field, CA, where she is currently a Senior Research Scientist. Her main research interests include carbon-nanotube- and nanowire-based sensors and electronic devices. She currently integrates nanoscale materials with micrometer-scale structures to make real workable nanodevices and nanosensors for electronics, optoelectronics, sensing, and imaging applications.



**Jun Li** received the B.S. degree in chemistry from Wuhan University, Wuhan, China, in 1987, and the M.S. and Ph.D. degrees in chemistry from Princeton University, Princeton, NJ, in 1991 and 1995, respectively.

From 1994 to 1997, he was a Post-Doctoral Research Associate with the Chemistry Department, Cornell University. From 1997 to 1998, he was with the Molecular Imaging Company. From 1998 to 2000, he was with the Institute of Materials Research and Engineering, Singapore. In September 2000, he joined the National Aeronautics and Space Administration (NASA) Ames Research Center, Moffett Field, CA, where he is currently a Physical Scientist with the Nanotechnology Center. He has coauthored over 40 papers in peer-reviewed scientific journals. He co-holds over eight patents in nanotechnology. His current research interests focus on the development of new methods to integrate the nanostructured materials to micro-sized and macro-sized devices in which the unique properties of individual nanoelements are utilized to improve the performance. He is currently involved with CNT nanoelectrode arrays targeting the development of ultrasensitive biosensors and the exploration of CNTs for IC interconnects.



**M. Meyyappan** (A'85–M'89–SM'96–F'04) is Director of the Center for Nanotechnology, as well as Senior Scientist with the National Aeronautics and Space Administration (NASA) Ames Research Center, Moffett Field, CA. He is a founding member of the Interagency Working Group on Nanotechnology (IWGN), which was established by the Office of Science and Technology Policy (OSTP). The IWGN is responsible for putting together the National Nanotechnology Initiative.

Dr. Meyyappan is the IEEE Distinguished Lecturer on Nanotechnology and Distinguished Lecturer on Nanotechnology of the American Society of Mechanical Engineers (ASME). He was the recipient of the NASA Outstanding Leadership Medal for his work on nanotechnology.



**Cary Y. Yang** (S'69–M'70–SM'84–F'99) received the B.S., M.S., and Ph.D. degrees in electrical engineering from the University of Pennsylvania, Philadelphia, in 1970, 1971, and 1975, respectively.

After working in various research positions at the Massachusetts Institute of Technology (MIT), Stanford University, and the National Aeronautics and Space Administration (NASA), he founded Surface Analytic Research Inc. and directed sponsored research in surface and nanostructure science. In 1983, he joined Santa Clara University, Santa Clara, CA, where he is currently a Professor of electrical engineering, Associate Dean of Engineering, and Director of the Center for Nanostructures. He has been a consultant to industry and government, and a Visiting Professor with the Tokyo Institute of Technology, Tokyo, Japan, the University of Tsukuba, Tsukuba, Japan, the National University of Singapore, Singapore, the University of Pennsylvania, and the University of California at Berkeley.

Dr. Yang served as regions/chapters chair, vice president, and president of the IEEE Electron Devices Society. He was an elected member of the IEEE Board of Directors, representing Division I (2002–2003). He was an editor of the IEEE TRANSACTIONS ON ELECTRON DEVICES in the area of MOS devices (1997–2000).